il Chip Access Chapter 9

- default æ)

vn (1µA, typical) default cted - default

 $\times$   $\times$   $\times$ 

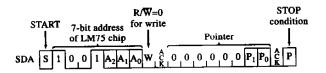
× × ×

× × ×

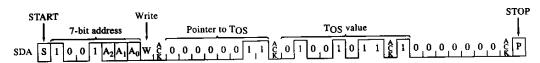
ring the second byte of a res are transmitted before be written into the selected mming of these bytes into

1, it will not acknowledge s a flag to determine when address with the write bit I it does get pulled low in intrepeatedly and the ACK ny bytes can take place as time to expire.

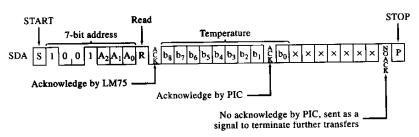
multaneously with the sin-P condition is received by ill eight addresses are con-EPROM's internal address te command sequence. For mand is B'00010110' and Section 9.6 Serial EEPROM



(a) Set pointer (and leave it set)



**(b)** Set  $T_{OS}$  to 75.5°C (2 x 75.5 = B'0100101111)



(c) Read temperature (with pointer previously set to temperature)

Figure 9-14 LM75 message strings.

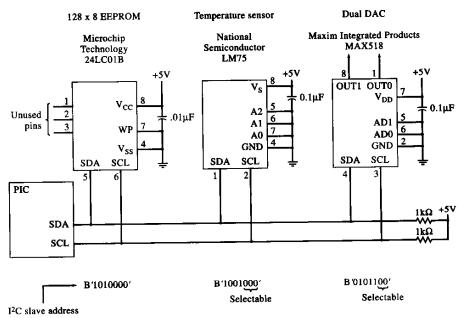


Figure 9-15 I<sup>2</sup>C bus connection to EEPROM chip and to other devices.