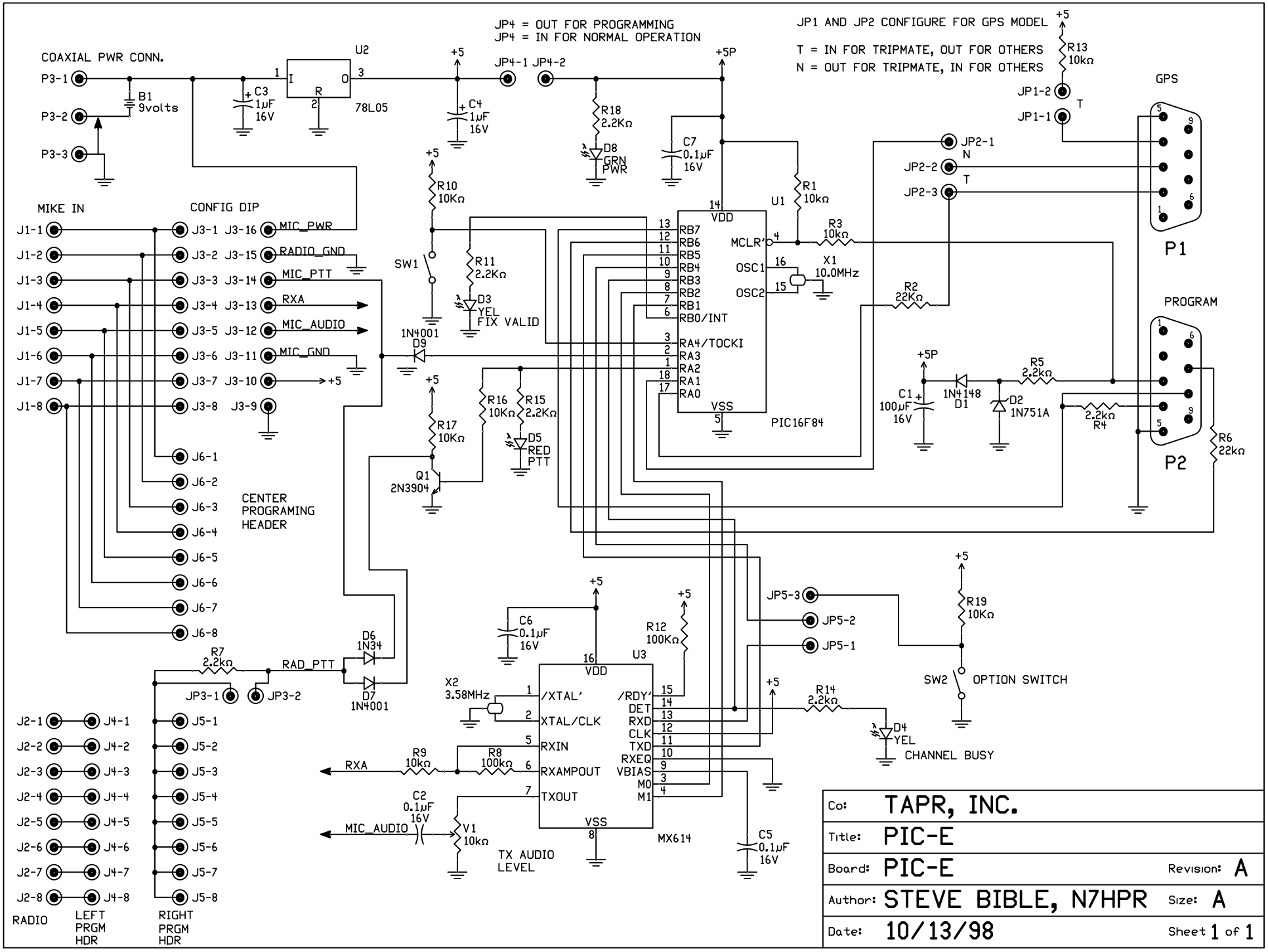
JP4 = OUT FOR PROGRAMMING
JP4 = IN FOR NORMAL OPERATION
JP1 AND JP2 CONFIGURE FOR GPS MODEL
T = IN FOR TRIPMATE, OUT FOR OTHERS
N = OUT FOR TRIPMATE, IN FOR OTHERS
COAXIAL PWR CONN.
P3-1
P3-2
P3-3
B1
9volts
U2
78L05
C3
1µF
16V
C4
1µF
16V
+5
+5P
JP4-1 JP4-2
R18
2.2KΩ
D8
GRN
PWR
C7
0.1µF
16V
R13
10kΩ
JP1-2
JP1-1
T
GPS
P1
JP2-1
N
JP2-2
T
JP2-3
MIKE IN
CONFIG DIP
J1-1
J1-2
J1-3
J1-4
J1-5
J1-6
J1-7
J1-8
J3-1
J3-2
J3-3
J3-4
J3-5
J3-6
J3-7
J3-8
J3-9
J3-16
J3-15
J3-14
J3-13
J3-12
J3-11
J3-10
MIC_PWR
RADIO_GND
MIC_PTT
RXA
MIC_AUDIO
MIC_GND
+5
R10
10KΩ
SW1
R11
2.2KΩ
D3
YEL
FIX VALID
1N4001
D9
U1
VDD
MCLR'
OSC1
OSC2
VSS
RB7
RB6
RB5
RB4
RB3
RB2
RB1
RB0/INT
RA4/TOCKI
RA3
RA2
RA1
RA0
PIC16F84
R1
10kΩ
R3
10kΩ
X1
10.0MHz
R2
22KΩ
PROGRAM
P2
R6
22kΩ
+5P
C1
100µF
16V
1N4148
D1
D2
1N751A
R5
2.2kΩ
2.2kΩ
R4
R16
10KΩ
R15
2.2KΩ
D5
RED
PTT
R17
10KΩ
Q1
2N3904
J6-1
J6-2
J6-3
J6-4
J6-5
J6-6
J6-7
J6-8
CENTER
PROGRAMING
HEADER
D6
1N34
D7
1N4001
R7
2.2kΩ
RAD_PTT
JP3-1
JP3-2
C6
0.1µF
16V
U3
VDD
X2
3.58MHz
/XTAL'
XTAL/CLK
RXIN
RXAMPOUT
TXOUT
VSS
/RDY'
DET
RXD
CLK
TXD
RXEQ
VBIAS
M0
M1
MX614
R12
100KΩ
R9
10kΩ
R8
100kΩ
RXA
C2
0.1µF
16V
MIC_AUDIO
V1
10kΩ
TX AUDIO
LEVEL
C5
0.1µF
16V
JP5-3
JP5-2
JP5-1
R19
10KΩ
SW2
OPTION SWITCH
R14
2.2kΩ
D4
YEL
CHANNEL BUSY
J2-1
J2-2
J2-3
J2-4
J2-5
J2-6
J2-7
J2-8
J4-1
J4-2
J4-3
J4-4
J4-5
J4-6
J4-7
J4-8
J5-1
J5-2
J5-3
J5-4
J5-5
J5-6
J5-7
J5-8
RADIO
LEFT
PRGM
HDR
RIGHT
PRGM
HDR
Co: TAPR, INC.
Title: PIC-E
Board: PIC-E
Revision: A
Author: STEVE BIBLE, N7HPR
Size: A
Date: 10/13/98
Sheet 1 of 1